

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination ADILETTA ET AL.	
		Examiner Barry J. O'Brien	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,992,934	02-1991	Portanova et al.	712/209
	B	US-5,450,603	09-1995	Davies, Daniel	712/22
	C	US-6,009,505	12-1999	Thayer et al.	712/6
	D	US-5,487,159	01-1996	Byers et al.	712/223
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Intel, IA-64 Application Developer's Architecture Guide, May 1999, Rev. 1.0, pgs. 2-2, 4-29 to 4-31, 7-116 to 7-118, and C-21.
	V	Hennessy et al., Computer Organization and Design: The Hardware/Software Interface, 1998, Morgan Kaufmann Publishers, pgs. 476-482.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.